

R2 Has SOI Finally Arrived?

Moderators: C.T. Chuang, *IBM*
Y. Ohtomo, *NTT Corp.*

SOI has recently emerged as a serious contender for low power high- performance applications. However, its wide acceptance by the electronics industry for mainstream applications is not apparent. This rump session brings together

- Partially-depleted vs fully-depleted devices
- Performance advantage over bulk technology
- How to handle “floating body” and “hysteresis” effects in a large scale design

experts in the area of SOI device technology, logic, memory, and analog design to discuss the issues for SOI in mainstream VLSI applications. Topics to be addressed include:

- SOI specific design and methodology
- Self-heating, reliability, ESD, and global design issues
- Analog/RF applications? Noise, matching and repeatability requirements.

R3 Visions of Computers in the Year 2005

Moderators: R. Allmon, *DEC*
T. Sakurai, *University of Tokyo*

Major advances in all technical disciplines of the computer industry are combining with a new global communication infrastructure to form a new era in computing. The

panelists will discuss how this technology will enable new applications and how the user will gain access to computers and information.